

Analog Electronic Circuits
Prof. Pradip Mandal
Department of Electronics and Electrical Communication Engineering
Indian Institute of Technology, Kharagpur

Lecture - 86
Numerical Examples on Current Mirror and its Applications (Part-A)

So dear students, so, welcome back to our online certification course on Analog Electronic Circuits. Myself, Pradip Mandal from E and EC Department of IIT, Kharagpur. Today's topic of discussion it is Numerical Examples on Current Mirror and some Application Circuits, where we are using current mirror. So, primarily we will be talking about numerical examples, to complement whatever the theory you have learnt on current mirror and its application circuit.

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Flow of Discussion (Bottom-up)
– Building blocks and Models

- **System /Sub-systems** (for specific application)
 - **Modules** (performing specific tasks)
 - **Building blocks** (having specific characteristics) - **Bias circuits**
 - Components (devices/circuit elements)
- **Week 9 (Course Module 8):**
 - ✓ **Current mirror**
 - operating principle and analysis,
 - ✓ **Use of current mirror**
 - as bias circuit and for signal amplification (in CE/CS, CC/CD, CB/CG and Differential amplifier).
 - ✓ **Use of current mirror**
 - as signal mirror (for current mode operation).

The slide also features a small video inset of Prof. Pradip Mandal in the bottom right corner and a Windows taskbar at the bottom.

Now according to our plan, we are in week 9 and in fact, that is the course module 8 and we have discussed about the theoretical aspect of current mirror and different application circuits. And as I said that today we will be covering numerical examples extensively, on current mirror and different types of current mirrors including simple one and then advanced one.

(Refer Slide Time: 01:35)

The slide features a dark blue background on the left with the text 'CONCEPTS COVERED' in yellow. The right side has a yellow background with the heading 'Concepts Covered:' in blue. Below this, there are three red square bullet points, each followed by a list of sub-topics. The first bullet point is 'Numerical examples on simple current mirror', with sub-points 'Using MOSFETs' and 'Using BJTs'. The second is 'Numerical examples on improvised CM', with sub-points 'Using BJTs' and 'Using MOSFETs'. The third is 'Numerical examples on amplifiers having CM', with sub-points 'CE/CS amplifiers' and 'Differential amplifiers'. A small video inset in the bottom right shows a man with glasses speaking. At the bottom of the slide, there are logos for IIT Bombay and IIT Madras, and a Windows taskbar.

CONCEPTS COVERED

Concepts Covered:

- ❑ **Numerical examples on simple current mirror**
 - ✓ Using MOSFETs
 - ✓ Using BJTs
- ❑ **Numerical examples on improvised CM**
 - ✓ Using BJTs
 - ✓ Using MOSFETs
- ❑ **Numerical examples on amplifiers having CM**
 - ✓ CE/CS amplifiers
 - ✓ Differential amplifiers

So, the coverage of today's presentation is enlisted here. So, we shall start with numerical examples of simple current mirror. We may start with current mirror having MOSFET transistor and then we can go for current mirror using BJT and then we will be moving to numerical examples on improvised current mirror or more precision current mirror.

And there also we will be having 2 types of circuits: namely one using BJT's and then other one is MOSFET. And subsequently, we will be talking about numerical examples on amplifiers which are using current mirror. So, our main focus is on the current mirror.

So, we may not be going into the aspect of the amplifiers, but primarily what are the advantages we can get using current mirror and what is the corresponding calculation we do? That will be highlighted by considering 2 specific types of examples, one is single-ended amplifier namely, common emitter and common source amplifier and then differential amplifier. Now, coming to a current mirror, simple current mirror constructed by MOSFET, here we do have the example.

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Numerical example on simple current mirror (using MOSFETs)

- Example 1:**
 - $(K_n \cdot W_1/L_1) = 1 \text{ mA/V}^2$, $(K_n \cdot W_2/L_2) = 4 \text{ mA/V}^2$; $V_{th1} = V_{th2} = 1.5 \text{ V}$,
 - $I_{REF} = 0.5 \text{ mA}$, $V_{DD} = 12 \text{ V}$
- (i) Considering $\lambda_1 = \lambda_2 \approx 0.0/\text{V}$,
 - Find the values of V_{GS1} and I_{DS2}
 - Find $V_{DS(\min)}$ for proper operation of the circuit

$$0.5 \text{ mA} = \frac{1 \text{ mA/V}^2}{2} (V_{GS1} - V_{th1})^2$$

$$V_{GS1} = V_{th1} + 1 = 2.5 \text{ V}$$

$$V_{DS(\min)2} = 2.5 \text{ V} - 1.5 \text{ V} = 1 \text{ V}$$

$$I_{DS2} = I_{DS1} \times \left(\frac{K_2}{K_1} \right) \left(\frac{W_1}{L_1} \right)$$

$$= 0.5 \text{ mA} \times \frac{4}{1}$$

$$= 2 \text{ mA}$$

So, here we do have the example circuit where M 1 and M 2 are forming current mirror. We do have a reference current here and then we do have the application circuit here. So in this

example, the W by L of transistor-1 and transistor-2 along with the K factor, it is given. For transistor-1, we do have 1 milliampere per volt square. For transistor-2, we do have 4 milliampere per volt square.

And let me assume that both the transistors are having equal threshold voltage of 1.5 and then we do have the reference current equals to half milliampere, and then supply voltage it is 12 volt. To start with, let we go simpler version, ignoring lambda effect considering both the lambdas are very small and let we try to find the values of V_{GS1} and I_{DS2} .

Of course, I_{DS1} it is same as I reference current namely, half milliampere. But then, V_{GS1} , it is setting V_{GS2} and since the W by L or K into W by L of the 2 transistors, they are different, we are expecting the current here to be different. So, let me start with the calculation of V_{GS1} or for I reference equals to 0.5, so 0.5 milliampere so, that is the I_{DS} that is equal to its corresponding K W by L , which is 1 milliampere per volt square by 2 into V_{GS1} minus V_{th} square.

So note that for this calculation, we are ignoring $1 + \lambda V_{DS}$. Even if the lambda is given, we normally ignore that. And if you see here, this equation it is giving us V_{GS1} equals to V_{th1} plus 1. So, that is equal to 2.5 volt. So, this 2.5 volt it is coming to the V_{GS} of transistor-2 and the corresponding current here, it is either we can use this information of 2.5 V_{GS} and then again, you can use the similar kind of equation. Or directly, we can use the expression of you know this I_{DS2} current.

In terms of I_{DS1} multiplied by W by L ratio of transistor-2 divided by W by L ratio of transistor-1. So, from this we can say that the I_{DS1} it is 1.5 milliampere and if you take the ratio of K into W by L , and K into W by L here. In fact, we do have K in here and K in here. So, they are getting cancelled and then W by L ratios are coming 4 by 1. So, that gives us I_{DS2} equals to 2 milliampere. So, current flow here it is 2 milliampere.

Now we can find next part it is that we need to find what is the minimum value of this V_{DS} of transistor-2, particularly for transistor-2 for proper operation of the circuit. Namely, the

current mirror current output current can be well defined by this equation only when transistor-2 also in saturation.

So to keep this transistor in saturation, we know that the drain voltage it should be higher than the gate voltage minus V_{th} and gate voltage we know, it is 2.5 volt. So, the $V_{DS\ min}$ equals to $V_{DS\ min}$ of transistor-2, it is equal to 2.5 volt, is the gate voltage minus V_{th} which is 1.5 volt. So, that gives us minimum requirement of this voltage it is 1 volt.

Now, let us consider the next part of the same question in the next slide. So, that will be continuation of this problem. But probably, we can consider some finite value of from this lambdas.

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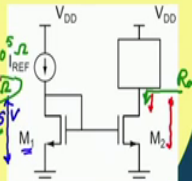
Numerical example on simple current mirror (using MOSFETs)

• Example 1 (contd.):

- $(K_n \cdot W_1/L_1) = 1 \text{ mA/V}^2$, $(K_n \cdot W_2/L_2) = 4 \text{ mA/V}^2$; $V_{th1} = V_{th2} = 1.5\text{V}$,
- $I_{REF} = 0.5 \text{ mA}$, $V_{DD} = 12\text{V}$

(ii) Considering $\lambda_1 = \lambda_2 \approx 0.01/\text{V}$,

- Find the values of I_{DS2} for $V_{DS2} = 2.5\text{V}$ and 5.5V
- Calculate the small signal output resistance of the current mirror



$$R_{out} = \frac{\Delta V}{\Delta I} = \frac{3\text{V}}{0.06\text{mA}} = \frac{1 \times 10^3 \Omega}{2} = 50 \text{ k}\Omega$$

$$I_{DS2} = \left[\frac{K_2}{L_2} \left/ \left(\frac{K_1}{L_1} \right) \right. \right] \times I_{REF} \times \frac{(1 + \lambda_2 V_{DS2})}{(1 + \lambda_1 V_{DS1})}$$


$$= 4 \times 0.5 = 2 \text{ mA}$$

$$V_{DS2} = 5.5\text{V}; \quad I_{DS2} \approx 2 \text{ mA} \frac{(1 + \lambda_2 V_{DS2})}{(1 + \lambda_1 V_{DS1})} = 2 \text{ mA} \left\{ 1 + \lambda (V_{DS2} - V_{DS1}) \right\}$$

$$= 2 \text{ mA} \times 1.03 = 2.06 \text{ mA}$$

$$I_{DS2} \approx 1 \text{ for } V_{DS2} = 2.5\text{V}$$

$$I_{DS2} = 2.06 \text{ mA}$$



So, in the next slide we do have, so, what we have here it is all the other parameters remaining same. In fact, it is continuation of the same example, but we are considering λ is equal to 0.01 per volt. And then again, we can try to find what is the corresponding value of this current. And particularly for 2 cases, if V_{DS2} here that is 2.5 and 5.5.

Now, you may recall from our previous calculation, the voltage here it is 2.5 V_{GS}, we already have calculated is equal to 2.5 and so that is also V_{DS} of transistor-1.

And then for the first case if V_{DS} equals to 2.5 and if you consider this λ , then you can get the current of I_{DS2} which is having this equation $\frac{W}{L}$ of transistor-2 divided by $\frac{W}{L}$ of transistor-1 multiplied by $I_{reference}$. And then we do have $1 + \lambda_1$ into sorry λ_2 into V_{DS2} divided by $1 + \lambda_1$ into V_{DS1} .

Now for this case, V_{DS2} equals to 2.5, both this V_{DS} and this V_{DS} they are same. So, and also the λ s are equal. So, we can say that this part of this equation it is becoming 1 for 2.5. As a result, the corresponding current here it is coming. So, this ratio it is 4 and $I_{reference}$ as I said it is 0.5. So, that gives us 2 milliamperes. And this is for V_{DS2} equals to 2.5 volt. And let us try to calculate the I_{DS2} for the other value namely the V_{DS} is equal to 5.5.

So for that, if I say V_{DS2} equals to 5.5, for this, I_{DS2} it is equal to so, this part it is remaining same. So, we can directly write that part which is 2 milliamperes multiplied by this non-ideality factor, $1 + \lambda_2 \frac{V_{DS2}}{V_{DS1}}$ divided by $1 + \lambda_1$. In fact, this part you can approximate well, approximate by considering this denominator factor into the numerator factor. And then if we ignore λ square term, and since both the λ s are equal, so, we can say that this is $1 + \lambda$ into V_{DS} of transistor-2 minus V_{DS} of transistor-1.

So, what we are getting here it is 2 milliamperes multiplied by so, $V_{DS2} - V_{DS1}$. So, V_{DS2} it is 5.5 and V_{DS1} it is 2.5. So, this part it becomes so, this part it becomes 3 and λ is 0.01, so that gives us 1.03 is the non-ideality factor. And that gives us the current

equals to 2.06 milliamperes. So, we can say that for these 2 different V_{DS} values we do have different currents; one is 2 milliamperes here, another is 2.06.

So pictorially, we may say that if we vary the V_{DS} of transistor-2, the current here it is having a finite slope. So, this is I_{DS2} and we do have one value here 2 for 2.5 volt the corresponding current is 2 milliamperes. On the other hand, for 5.5 we do have so, this current is 2.06 milliamperes. And from the slope of this line from the slope of this line, we can calculate the output resistance or we can strictly speaking, it is small signal output resistance.



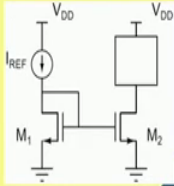
So, to calculate the small signal output resistance what we can see here it is we can get the calculate the slope of this line and reciprocal of that is the small signal output resistance. So, small signal output resistance at the output of the current mirror R_{out} . So, that is the voltage difference ΔV divided by ΔI and we know that ΔV it is 3 volt and the corresponding variation of this ΔI it is 0.06 milliamperes.

And so, this is giving us how much? $1/2 \times 10^5$ ohms, right. Or you can say this is 0.5, 0.5 and then in fact, 50 kilohms. So that is the small signal output resistance of 50 kilohms, right. So, now, if we continue this exercise for say, other condition. So, let us see in the next slide the third part of this, now we are going to BJT. So, we have considered this simple current mirror. So, similar kind of circuit can be constructed by BJT.

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Numerical example on simple current mirror (using MOSFETs)

- **Example 1 (contd.):**
- $(K_n \cdot W_1/L_1) = 1 \text{ mA/V}^2$, $(K_n \cdot W_2/L_2) = 4 \text{ mA/V}^2$; $V_{th1} = V_{th2} = 1.5\text{V}$,
- $I_{REF} = 0.5 \text{ mA}$, $V_{DD} = 12\text{V}$
- (ii) Considering $\lambda_1 = \lambda_2 = 0.01/\text{V}$,
 - Find the values of I_{S2} for $V_{DS2} = 2.5\text{V}$ and 5.5V
 - Calculate the small signal output resistance of the current mirror



So, in the next slide will be going for simple current mirror constructed by BJT's and it will be having similar kind of exercise. But of course, the corresponding parameter of the BJT's are different. So, let us see in the next example how we calculate for current mirror circuit constructed by BJT.

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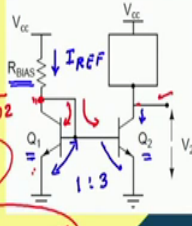
Numerical example on simple current mirror (using BJTs)

• Example 2:

- Assume that, $V_{BE(on1)} = V_{BE(on2)} \approx 0.6V$
- $I_{s1} = 9.5 \times 10^{-14}$ Amp, $I_{s2} = 2.85 \times 10^{-13}$ Amp, $\rightarrow \frac{I_{s2}}{I_{s1}} = 3$
- $V_{cc} = 12V$; $R_{BIAS} = 22.8 k\Omega$

✓(i) Considering β_1, β_2, V_{A1} and V_{A2} are having very high values, calculate I_{C2} . Also, find $V_2(\min)$ for proper operation of the circuit

✓(ii) Considering $\beta_1 = 100, \beta_2 = 150$ and, V_{A1} and V_{A2} are having very high values, calculate $I_{C2} = 1.456 mA$



$$I_{REF} = \frac{12V - 0.6V}{22.8 k\Omega} = \frac{11.4}{22.8} mA = 0.5 mA$$

$$I_{C2} = \frac{I_{s2}}{I_{s1}} \times I_{REF} \left[\frac{\beta_1}{\beta_1 + 1} \right] = 3 \times 0.5 mA \times \frac{1}{1.03} = 1.456 mA$$

Now, in this example we do have Q 1 and Q 2. Now it is forming the current mirror and in this case, just for a change, instead of giving a reference current, we are giving a resistor here, supply voltage it is given to us 12 volt. This R BIAS register in resistance it is 22.8 kilohm and then we can assume that V BE on voltage for both the transistors are approximately 0.6 volt. In addition to that, we also have the information about reverse saturation current of the 2 transistors.

So, Q 1 it is having reverse saturation current of 9.5×10^{-14} ampere. On the other hand, for Q 2 we do have reverse saturation current which is 2.85×10^{-13} ampere. In fact, if you see carefully, this is I_{S2} / I_{S1} is equal to 3. So, that is how I have picked up the number here.

So with this, what we can probably we can see the mirroring ratio it will be 1 is to 3 if it is, if we approximate that early voltage and then beta loss or the base current loss it is ignorable, then we can say this current mirror is essentially 1 is to 3 ratio current mirror. But before that we need to find what is the reference current itself $I_{reference}$.

To get thus this $I_{reference}$ current, so we need to find what will be the $I_{reference}$? $I_{reference}$ is equal to 12 volt V_{CC} minus V_{BE} or 0.6 volt divided by 22.8 kilohms. So, we do have 11.4 divided by 22.8 k. So that much of milliamperes which is 0.5 milliamperes. Now, if I consider a simple situation considering this both the beta's are very high, early voltages they are also very high which means that non-ideality factor we can we are approximating equal to 1.

So, the current flow here I_{C2} is given by I_{S2} divided by I_{S1} multiplied by this $I_{reference}$ current. And then we do have the 2 non-ideality factors. One may be due to early voltage another one may be due to the base current loss or due to finite beta. And for this part, both these non-ideality factors are equal to 1. So, this is equal to 1 this is also equal to 1.

So, that gives us the current I_{C2} equals to I_{S2} by I_{S1} which is 3 and then $I_{reference}$ current we have calculated is 0.5. So, that gives us the output current equals to 1.5 milliamperes.

Now, let us consider the effect of beta namely, the current loss due to whatever the currents are flowing here. So that means, this is no more equal to 1 and to get this non-ideality factor you may recall that this part is equal to $1 + \frac{1}{\beta_1} + \frac{1}{\beta_2}$ plus I_{S2} divided by I_{S1} into $1 + \frac{1}{\beta_1} + \frac{1}{\beta_2}$. So, this is the non-ideality factor and the values of β_1 and β_2 's are given. This is also the I_{S2} by I_{S1} , it is also known that is why this is 3.

So, we can see that let me use this space. So, this non-ideality factor it becomes $1 + \frac{1}{150} + \frac{1}{50}$. Then we do have 3 here and then 150 in the denominator. So, that gives us $1 + \frac{1}{150} + \frac{1}{50}$ so, that is equal to 1.03, ok.

So with this factor, for this case, to calculate this current we need to multiply by this non-ideality factor of 1.03. In fact, if you calculate it what we are getting here it is 1.5 divided by 1.03 so, that is equal to 1.456 milliampere. So, this part it is coming 1.456 milliampere.

So if I consider beta, finite beta of course, this current is getting smaller because this non-ideality factor it is less than 1. On the other hand, if I consider say early voltage if this voltage and this voltage they are not equal, then again we will be getting non-ideal adding the second non-ideality factor. So, if this voltage it is higher than the V CE of transistor-1 or , so, in that case this non ideal non second non-ideality factor it may be higher than 1. So, to consider that let we go for the third part of this example.

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Numerical example on simple current mirror (using BJTs)

• **Example 2 (contd.):**

- Assume that, $V_{BE(on)1} = V_{BE(on)2} \approx 0.6V$,
- $I_{S1} = 9.5 \times 10^{-14}$ Amp, $I_{S2} = 2.85 \times 10^{-13}$ Amp,
- $V_{cc} = 12V$; $R_{BIAS} = 22.8 k\Omega$

(iii) Considering $\beta_1 = 100$, $\beta_2 = 150$ and $V_{A1} = 50V$ and $V_{A2} = 100V$, calculate I_{C2} for $V_2 = 0.6V$ and $5.6V$. Also, calculate small signal output resistance

$R_{out} = \frac{\Delta V}{\Delta I} = \frac{5}{0.072} \times 10^3 \Omega = 69.4 k\Omega$

$I_{C2} = 1.5 \times \frac{1}{1.03} \text{ mA} = 1.456 \text{ mA} \times 0.994 = 1.447 \text{ mA}$

Handwritten calculations for I_{C2} at $V_2 = 0.6V$ and $5.6V$ are shown, including the use of the Early voltage model:

$$I_{C2} = I_{C1} \left(\frac{1 + \frac{V_{CE2}}{V_{A2}}}{1 + \frac{V_{CE1}}{V_{A1}}} \right)$$

For $V_2 = 0.6V$, $I_{C2} \approx 1.447 \text{ mA}$. For $V_2 = 5.6V$, $I_{C2} \approx 1.519 \text{ mA}$.

So, in the next slide it is continuation of the same numerical problem. So as you can see here, we do have all the parameters we are keeping same. Except, we do have early voltage of the 2

transistors are given here and intentionally, we are using different values of early voltage. And we already have obtained the effect of beta, right.

So, we already got I_{C2} equals to 1.5 multiplied by 1 by 1.03 which in fact so, this is equal to 1.456 milliamper, without considering this early voltage. Now, if we consider this early voltage and if you observe the V_{CE} voltage difference, then we have we can calculate the that factor.

So, let me consider V_{B2} , this V_{B2} it is equal to 0.6 volt and we know that this is V_{CE1} , V_{CE1} equals 0.6 volt. So, if I consider this V_{B2} or V_{B2} equals to 0.6 then that non-ideality factor, which is $1 + \lambda_{2} V_{CE1}$ multiplied, sorry, I am going to (Refer Time: 27:29) $1 + V_{CE2}$ divided by V_{A2} divided by $1 + V_{CE1}$ divided by V_{A1} . And for this case, both this part is 0.6 and also this part is 0.6.

However, this early voltage we are keeping it is same. So, it is not getting cancelled, it is rather we do have $1 + 0.006$ in the numerator and in the denominator, we do have $1 + 0.006$ divided by 50. So, that is equal to 0.12, ok.

So, this non-ideality factor it is 1.006 divided by one point sorry 1.006 divided by 1.012. So, this is 0.994. So, we need to consider this factor of 0.994 and so that gives us the current equals to 1.447 milliamper. This is the case with V_{B2} is equal to 0.6. Now if I consider say V_{B2} is equal to 5.6 so, in that case what are the changes do we expect? This part this part it will be different, namely, and that part it will be 5.6 divided by 100.

So, that it becomes 0.056. In fact, this factor then it will be 1.056 divided by 1.012. So, that is equal to 1.0435. So, instead of 0.994, we need to replace this by 1.0435, and the corresponding current here instead of 1.447, we do have 1.456 multiplied by this factor. So, that is equal to 1.519 so, this much of milliamper. So, for this voltage we do have 1.59 milliamper.

Again to you can use this to data point to calculate the slope of the I_C versus V_{CE} . So, at this point of 0.6 volt V_{CE2} , we do have the current that current it was this was point 1.447.

On the other hand, at this point we do have current which is for 5.6 volt V_{CE2} and this voltage this current it is 1.519 milliamperes. So again, by considering reciprocal of this slope we can calculate the output resistance offered by this current mirror at its output.

So, R_{out} so, that is equal to ΔV voltage change divided by the corresponding current change. And voltage change it is 5 volt; 5.6 minus 0.6 so, that is 5 volt divided by this current difference. So, that is equal to 1.519 minus 1.447. So, that is 0.072 milli. So that means, it is 10^3 ohms or you can say that this is equal to 69.4 kilohms.

So, the small signal output resistance it is 59.4 kilohms. So now, here we have considered, so far we have considered simple current mirror. Now we can go for more precision current mirror and as we can see here, if I consider finite values of β 's and then early voltage, the ratio instead of 1 is to 3, it is becoming different, slightly different though. There may be some precision cases, precision applications where this much of difference may not be still acceptable.

And then we can go for betterment of the circuit. So, for current mirror constructed by BJT's we do have 2 types of improvement one is to take care of the non-ideality due to the early voltage another one it is to take care of the non-idealities factor due to the base current loss. So to take care of the base current loss, as we have said that we can have a beta-helper circuit here, so that the current loss to the base of this third transistor which is much smaller than whatever the base current is going to Q_1 and Q_2 which is referred as beta-helper.

So, continuation of this new this numerical example, to go for that beta-helper, let us go to a next slide.

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Numerical example on improved current mirror (using BJTs)

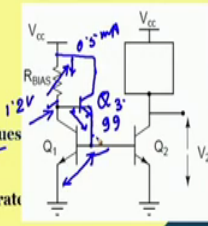
• **Example 3:**

- Assume that, $V_{BE(on)1} = V_{BE(on)2} \approx 0.6V$,
- $I_{s1} = 9.5 \times 10^{-14}$ Amp, $I_{s2} = 2.85 \times 10^{-13}$ Amp,
- $V_{cc} = 12V$; $R_{BIAS} = 22.8 k\Omega$
- Consider $\beta_1 = 100$, $\beta_2 = 150$ and, V_{A1} and V_{A2} are having very high values

(i) Calculate the non-ideality factor due base current loss.

(ii) Add a Beta helper transistor (Q3) in the current mirror for more accurate current mirror ratio.

Also, adjust the value of R_{BIAS} to $21.6 k\Omega$ to keep the reference current same. Calculate the non-ideality factor and then I_{C2} assuming $\beta_3 = 99$



The diagram shows a current mirror circuit with three BJTs: Q1, Q2, and Q3. Q1 is the reference transistor, Q2 is the output transistor, and Q3 is a beta-helper transistor. The base-emitter junction of Q1 is connected to a resistor R_BIAS and V_BE. The collector of Q1 is connected to V_CC. The base of Q2 is connected to the collector of Q1. The emitter of Q2 is connected to the base of Q3. The emitter of Q3 is connected to the base of Q1. The collector of Q3 is connected to the collector of Q2. The circuit is powered by V_CC and ground. A current of 0.5 mA is indicated at the collector of Q1. The output current I_C2 is shown at the collector of Q2.

So it is, I should say it is more like a continuation of the previous problem. Namely, we are retaining the parameters here. So, the current flow here it is 0.5 milliamper and we have we have calculated the base current the non-ideality factor due to the base current loss. Now we can improvise the circuit by using beta-helper here.

So, let you call this is transistor-3. So, we are adding this beta-helper and this beta-helper its beta it is 99 and then once we add this transistor, we need to readjust this register because once you add this transistor, since the collector and base voltage they are not same, in fact, if you observe carefully, this is one V_{BE} this is another V_{BE} .

So, if I approximate that this V_{BE} it is also 0.6, then the DC voltage coming here it is 1.2 volt, ok. Now since you are trying to keep the focus only on the non-ideality factor coming due to the base current loss, we are suppressing the other information namely, in this

example, we are considering early voltage it is a very high. And we are primarily focusing on this base current loss. So, in the next slide we do have the beta-helper circuit.

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Numerical example on improved current mirror (using BJTs)

• **Example 3 (contd.) :**

- Assume that, $V_{BE(on)1} = V_{BE(on)2} \approx 0.6V$,
- $I_{s1} = 9.5 \times 10^{-14}$ Amp, $I_{s2} = 2.85 \times 10^{-13}$ Amp,
- $V_{cc} = 12V$; $R_{BIAS} = 22.8 \text{ k}\Omega$
- Consider $\beta_1 = 100$, $\beta_2 = 150$ and, V_{A1} and V_{A2} are having very high values,

(ii) Add a Beta helper transistor (Q_3) in the current mirror for more accurate current mirror ratio.

Also, adjust the value of R_{BIAS} to $21.6 \text{ k}\Omega$ to keep the reference current same. Calculate the non-ideality factor and then I_{C2} assuming $\beta_3 = 99$

Handwritten calculations:

$$I_{REF} = \frac{12 - 1.2}{R_{BIAS}} = \frac{10.8}{21.6} = 0.5 \text{ mA}$$

Non-ideality factor:

$$\text{Non-ideality factor} = \frac{1}{(1 + 0.0003)} \approx 1$$

$$= \frac{1}{(1 + 0.01)[0.01 + 0.02]} = \frac{1}{(1 + 0.01)[0.03]} = \frac{1}{0.0303} \approx 33$$

So, here we do have the beta-helper circuit drawn for you. We do have Q_3 here and as I said there this is approximately 0.6 and here also, it is approximately 0.6. So, we do have 1.2 volt. So what we are doing here, to retain this current of 0.5 milliamperes in this numerical example, we are readjusting this R_{BIAS} to 21.6 kilohms. And that gives us the I_{REF} current, it is same as 0.5.

So, let us see that our I_{REF} equals to 12 volt minus 1.2 volt here divided by this R_{BIAS} and so, this is equal to 10.8, 10.8 divided by 21.6. So, that gives us 0.5 milliamperes. So for our comparison, better comparison we are keeping this reference current same as the previous case. And then we can calculate the corresponding non-ideality factor.

So, you may recall the non-ideality factor in presence of the beta the beta-helper is. So, non-ideality factor it becomes $1 \div (1 + \beta_1 + \beta_2 + \beta_3)$ multiplied by $I_{S2} \div I_{S1}$ multiplied by $1 + \beta_2$. And earlier, we have calculated in absence of this one. Now, we do have $1 + \beta_1$ so, this part $99 + 1$. So, this is 100.

So, that gives us a factor here 0.01 getting multiplied with beta 1 it is 100. So, $1 + \beta_1$ it 0.00 sorry 01 plus this one it is 150. So, that is equal to 0.02, right. And so this is how much? This 0.03. So, that is equal to non-ideality factor. So, it becomes $1 \div (1 + 0.0003)$ and this is very very small compared to 1.

So, we can approximate that this is equal to 1. In fact, how much is it coming? This is 1.0003 reciprocal so, that is equal to in fact, 0.9997. So, that is how this beta-helper is helping us to maintain this ratio here, it is very close to 1 is to 3. So, similar to so, this circuit as I said that it is improvised current mirror. Similar to this improvisation, we do have different improvisation by adding something called cascode transistor to make the non-ideality factor due to early voltage it will be very small.

So, we will be discussing that circuit, but before that let we take a break and we will come back.

Thank you.